

Intel® 21555 Non-Transparent PCI-to-PCI Bridge Evaluation Board

Release Notes

11/27/02

Order Number: 278656-001

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1.0 Description

The Intel® 21555 non-transparent PCI-to-PCI bridge is a PCI peripheral device that performs PCI bridging functions for embedded and intelligent I/O applications. The 21555 has a 64-bit primary interface, a 64-bit secondary interface, with 33 MHz and 66 MHz capability. A related PCI peripheral device referenced for comparison purposes, has a 64-bit primary interface, a 64-bit secondary interface, and 33 MHz capability.

1.1 Package Contents

This release of the 21555 bridge package consists of the following items:

- 21555_releasenote.pdf – Release Note that describes the contents of the package (this file).
- 278654.pdf - describes the DbFlash utility
- DbFlash.exe
- 278655.pdf – describes the MKSROM utility
- MKSROM.exe
- PView.txt – Readme file that describes the utility
- PView.exe
- 278359_evalguid.pdf (*Intel® 21555 Non-transparent PCI-to-PCI Bridge User's Guide*) – This document describes the switches, jumpers, and connectors on 21555 bridge physical hardware.
- Schematic.pdf - Intel® 21555 Non-transparent PCI-to-PCI Bridge Schematic
- 21555 Training.pdf
- DOS4GW.txt – Readme file that explains the utility
- dos4gw.exe

1.2 Related Documentation and Updates

Documentation that may be helpful:

- *Intel® 21555 Non-transparent PCI-to-PCI Bridge Data Sheet (278320)* – This document describes the product features of the 21555 bridge.
- *Intel® Non-transparent PCI-to-PCI Bridge Specification Update* – This document describes changes and updates to the 21555 product.
- URL to 21555 Bridge documentation: http://developer.intel.com/design/bridge/docs/21555_documentation.htm

Software and Hardware Updates:

- For the latest software and hardware updates, contact your Application Engineer (AE).

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